

Code No: 58050

Set No. 1

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

IV B.Tech. II Sem., II Mid-Term Examinations, April-2014

PC BASED INSTRUMENTATION

Objective Exam

Name: _____ Hall Ticket No.

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Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

I. Choose the correct alternative:

1. Which register is readily accessible to the input module terminals or ports? []
(A) Input (B) Output (C) Holding (D) all the above
2. An output group register of length 16 bits, can control _____ outputs. []
(A) 4 (B) 6 (C) 8 (D) 16
3. Within the PLC CPU registers are found in _____ locations. []
(A) four (B) three (C) two (D) five
4. In PLCs _____ is used to count input pulses or events. []
(A) timer (B) counter (C) register (D) coil
5. Early PLCs were programmed using _____. []
(A) Basic (B) assembly (C) ladder logic (D) binary logic
6. SKIP function of a PLC is similar to _____ instruction. []
(A) CALL (B) JUMP (C) RET (D) ADD
7. A PLC AND matrix function operates on the same principle as a _____ circuit. []
(A) adder (B) parallel (C) series (D) multiplier
8. Which PLC programming system uses lots of IF, ELSE and TRUE or FALSE descriptions? []
(A) Ladder diagram (B) Functional block diagram (C) Structured text (D) All the above
9. FORCE procedure is normally carried out in _____ mode. []
(A) open (B) closed (C) FORCE (D) MONITOR
10. In HART protocol _____ is used for coding digital information. []
(A) PSK (B) field wiring (C) signal conditioning (D) FSK

Cont.....2

II Fill in the Blanks:

11. Each complete horizontal line of a ladder diagram is generally referred as a _____.
12. In ladder diagram two vertical lines represents _____.
13. Allen Bradley PLCs use _____ instruction to perform arithmetic functions.
14. A typical PLC chart will use a _____ to represent an energized output and a _____ to indicate a de energized output.
15. The most commonly used type of I/O interface in PLCs is _____ .
16. The precision with which analog I/O module in a PLC handle voltages depends on _____.
17. MCR stands for _____.
18. Smart sensors are known as _____ devices.
19. Field bus consists of _____ types of communication buses.
20. FIP means _____.

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Code No: 58050

Set No. 2

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

IV B.Tech. II Sem., II Mid-Term Examinations, April-2014

PC BASED INSTRUMENTATION

Objective Exam

Name: _____ Hall Ticket No.

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Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

I. Choose the correct alternative:

1. In PLCs _____ is used to count input pulses or events. []
(A) timer (B) counter (C) register (D) coil
2. Early PLCs were programmed using _____. []
(A) Basic (B) assembly (C) ladder logic (D) binary logic
3. SKIP function of a PLC is similar to _____ instruction. []
(A) CALL (B) JUMP (C) RET (D) ADD
4. A PLC AND matrix function operates on the same principle as a _____ circuit. []
(A) adder (B) parallel (C) series (D) multiplier
5. Which PLC programming system uses lots of IF, ELSE and TRUE or FALSE descriptions? []
(A) Ladder diagram (B) Functional block diagram (C) Structured text (D) All the above
6. FORCE procedure is normally carried out in _____ mode. []
(A) open (B) closed (C) FORCE (D) MONITOR
7. In HART protocol _____ is used for coding digital information. []
(A) PSK (B) field wiring (C) signal conditioning (D) FSK
8. Which register is readily accessible to the input module terminals or ports? []
(A) Input (B) Output (C) Holding (D) all the above
9. An output group register of length 16 bits, can control _____ outputs. []
(A) 4 (B) 6 (C) 8 (D) 16
10. Within the PLC CPU registers are found in _____ locations. []
(A) four (B) three (C) two (D) five

Cont.....2

II Fill in the Blanks:

11. A typical PLC chart will use a _____ to represent an energized output and a _____ to indicate a de energized output.
12. The most commonly used type of I/O interface in PLCs is _____ .
13. The precision with which analog I/O module in a PLC handle voltages depends on _____.
14. MCR stands for _____.
15. Smart sensors are known as _____ devices.
16. Field bus consists of _____ types of communication fuses.
17. FIP means _____.
18. Each complete horizontal line of a ladder diagram is generally referred as a _____.
19. In ladder diagram two vertical lines represents _____.
20. Allen Bradley PLCs use _____ instruction to perform arithmetic functions.

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Code No: 58050

Set No. 3

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

IV B.Tech. II Sem., II Mid-Term Examinations, April-2014

PC BASED INSTRUMENTATION

Objective Exam

Name: _____ Hall Ticket No.

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Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

I. Choose the correct alternative:

1. SKIP function of a PLC is similar to _____ instruction. []
(A) CALL (B) JUMP (C) RET (D) ADD
2. A PLC AND matrix function operates on the same principle as a _____ circuit. []
(A) adder (B) parallel (C) series (D) multiplier
3. Which PLC programming system uses lots of IF, ELSE and TRUE or FALSE descriptions? []
(A) Ladder diagram (B) Functional block diagram (C) Structured text (D) All the above
4. FORCE procedure is normally carried out in _____ mode. []
(A) open (B) closed (C) FORCE (D) MONITOR
5. In HART protocol _____ is used for coding digital information. []
(A) PSK (B) field wiring (C) signal conditioning (D) FSK
6. Which register is readily accessible to the input module terminals or ports? []
(A) Input (B) Output (C) Holding (D) all the above
7. An output group register of length 16 bits, can control _____ outputs. []
(A) 4 (B) 6 (C) 8 (D) 16
8. Within the PLC CPU registers are found in _____ locations. []
(A) four (B) three (C) two (D) five
9. In PLCs _____ is used to count input pulses or events. []
(A) timer (B) counter (C) register (D) coil
10. Early PLCs were programmed using _____. []
(A) Basic (B) assembly (C) ladder logic (D) binary logic

Cont.....2

II Fill in the Blanks:

11. The precision with which analog I/O module in a PLC handle voltages depends on _____.
12. MCR stands for _____.
13. Smart sensors are known as _____ devices.
14. Field bus consists of _____ types of communication buses.
15. FIP means _____.
16. Each complete horizontal line of a ladder diagram is generally referred as a _____.
17. In ladder diagram two vertical lines represents _____.
18. Allen Bradley PLCs use _____ instruction to perform arithmetic functions.
19. A typical PLC chart will use a _____ to represent an energized output and a _____ to indicate a de energized output.
20. The most commonly used type of I/O interface in PLCs is _____ .

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Code No: 58050

Set No. 4

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

IV B.Tech. II Sem., II Mid-Term Examinations, April-2014

PC BASED INSTRUMENTATION

Objective Exam

Name: _____ Hall Ticket No.

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Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

I. Choose the correct alternative:

1. Which PLC programming system uses lots of IF, ELSE and TRUE or FALSE descriptions? []
(A) Ladder diagram (B) Functional block diagram (C) Structured text (D) All the above
2. FORCE procedure is normally carried out in _____ mode. []
(A) open (B) closed (C) FORCE (D) MONITOR
3. In HART protocol _____ is used for coding digital information. []
(A) PSK (B) field wiring (C) signal conditioning (D) FSK
4. Which register is readily accessible to the input module terminals or ports? []
(A) Input (B) Output (C) Holding (D) all the above
5. An output group register of length 16 bits, can control _____ outputs. []
(A) 4 (B) 6 (C) 8 (D) 16
6. Within the PLC CPU registers are found in _____ locations. []
(A) four (B) three (C) two (D) five
7. In PLCs _____ is used to count input pulses or events. []
(A) timer (B) counter (C) register (D) coil
8. Early PLCs were programmed using _____. []
(A) Basic (B) assembly (C) ladder logic (D) binary logic
9. SKIP function of a PLC is similar to _____ instruction. []
(A) CALL (B) JUMP (C) RET (D) ADD
10. A PLC AND matrix function operates on the same principle as a _____ circuit. []
(A) adder (B) parallel (C) series (D) multiplier

Cont.....2

II Fill in the Blanks:

11. Smart sensors are known as _____ devices.
12. Field bus consists of _____ types of communication buses.
13. FIP means _____.
14. Each complete horizontal line of a ladder diagram is generally referred as a _____.
15. In ladder diagram two vertical lines represents _____.
16. Allen Bradley PLCs use _____ instruction to perform arithmetic functions.
17. A typical PLC chart will use a _____ to represent an energized output and a _____ to indicate a de energized output.
18. The most commonly used type of I/O interface in PLCs is _____ .
19. The precision with which analog I/O module in a PLC handle voltages depends on _____.
20. MCR stands for _____.

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