

Code No: 56012

Set No. 1

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

III B.Tech. II Sem., II Mid-Term Examinations, April – 2014

MICRO PROCESSORS AND MICRO CONTROLLERS

Objective Exam

Name: _____ **Hall Ticket No.**

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Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

I Choose the correct alternative:

1. Theduplex transmission allows the data transfer in both directions but not simultaneously []
a) Half duplex b) Full duplex c) Simplex d) all
2. communication is basically used for long distance communication []
a) Parallel b) Serial c) Both a and b d) None
3. formats are character oriented []
a) Synchronous b) Asynchronous c) Both a and b d) None
4. The size of on-chip program memory for 8051 is []
a) 4K bytes b) 8K bytes c) 16K bytes d) 32K bytes
5. 8051 has user-defined software flags andkilobytes each program and external RAM addressability. []
a) 64,128 b) 32, 64 c) 128, 64 d) 64,32
6. In the mode of 8051, the register is incremented after every machine cycle. []
a) Timer b) Counter c) Both a and b d) None
7. Which of the following 8051 interrupt has highest priority []
a) Timer 0 b) Timer 1 c) External 0 d) External 1
8. Which of the following ports of 8051 might be designated as a true bi-directional port []
a) Port 2 b) Port 1 c) Port 0 d) Port 3
9. AVR microcontrollers are architecture microcontrollers. []
a) CISC b) RISC c) both a and b d) None
10. Serial communication of 8051 uses modulation. []
a) Amplitude b) Frequency c) Phase d) all

Cont.....2

II Fill in the blanks

11.means the rate of information communication, generally expressed per second.
12.is often referred to as the General Purpose Interface Bus (GPIB)
13. The devices which provide synchronous as well as asynchronous transmission and receives are called.....
14. The 8051 provides four programmable modes fordata communication.
15. Interrupts release the processor for its other duties rather than continuously watching an input to be true. This is known as
16.were developed by ATMEL which has flash memory for program storage.
17.is one of the most important special function register in 8051 through which the status of operations performed by the CPU is reflected.
18. 8051 offers full-duplexserial communication feature through its external pins P3.0 and P3.1
19.is automatically incremented to point the next instruction in the program sequence after execution of the current instruction.
20.standard describes the functions of 25 signal and handshake pins for serial data transfer.

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Set No. 2

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10. formats are character oriented []
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Cont.....2

II Fill in the blanks

11. The 8051 provides four programmable modes fordata communication.
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Set No. 3

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MICRO PROCESSORS AND MICRO CONTROLLERS

Objective Exam

Name: _____ Hall Ticket No.

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a) 64,128 b) 32, 64 c) 128, 64 d) 64,32

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II Fill in the blanks

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9. In the mode of 8051, the register is incremented after every machine cycle. []
a) Timer b) Counter c) Both a and b d) None
10. Which of the following 8051 interrupt has highest priority []
a) Timer 0 b) Timer 1 c) External 0 d) External 1

Cont.....2

II Fill in the blanks

11. 8051 offers full-duplexserial communication feature through its external pins P3.0 and P3.1
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