

Code No: 56056

Set No. 1

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

III B.Tech. II Sem., II Mid-Term Examinations, April – 2014

EMBEDDED SYSTEMS

[IT]

Objective Exam

Name: _____ Hall Ticket No.

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Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

I Choose the correct alternative:

1. Control sequence of D/A conversion is []
a. CS then WR b. WR then CS c. CS then WR then RD d. WR then RD
2. A/D conversion time is []
a. 1 μ s b. 2 μ s c. 3 μ s d. 4 μ s
3. A/D converters use ____ types. []
a. SAR b. flash c. magnetic d. bubble
4. A ____ RTOS will stop a lower-priority task as soon as the higher-priority task unblocks. []
a. preemptive b. non preemptive c. interpret d. exception
5. The program ____ displays characters found in locations ch1 to ch4 on four common-cathode seven- segment displays. []
a. svnseg b. lcdisp c. lcdisp2 d. svnseg3
6. A ____ keyboard is interfaced with 8051 microcontroller. []
a. 16-key b. 32-key c. 64-key d. 128-key
7. Which serial data communication mode is multiprocessor 9-bit UART []
a. mode 0 b. mode 1 c. mode 2 d. mode 4
8. For both counting semaphore and binary semaphore a _____ is used to hold processing waiting on the semaphores []
a. stack b. queue c. dequeue d. circular queue
9. ARM comparison instruction is []
a. MOV b. MVN c. LDR d. TEQ
10. "Overflow" condition code in ARM is []
a. VC b. VS c. EQ d. EZ

Cont.....2

II Fill in the blanks

11. The _____ program can be modified to use a timer to generate associated with the key-down de bounce time and the "all-up" delay.
12. _____ displays include numeric and alphanumeric arrays.
13. A SHARC instruction consists of _____ bits
14. The barber problem is an example of _____
15. To access a shared resource, there should be a mechanism so that there is discipline. This is known as _____ synchronization
16. Counting semaphore will have an integer value greater than _____
17. A _____ mechanism is used to get your software into your target for debugging purposes.
18. A _____ understands the same C language as a native compiler and as assembly language that is specific to your target microprocessor.
19. BDM stands for _____
20. The _____ state means that this task has not got anything to do right now, even if the microprocessor becomes available.

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Set No. 2

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Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

I Choose the correct alternative:

1. A ____ RTOS will stop a lower-priority task as soon as the higher-priority task unblocks. []
a. preemptive b. non preemptive c. interpret d. exception
2. The program ____ displays characters found in locations ch1 to ch4 on four common-cathode seven- segment displays. []
a. svnseg b. lcdisp c. lcdisp2 d. svnseg3
3. A ____ keyboard is interfaced with 8051 microcontroller. []
a. 16-key b. 32-key c. 64-key d. 128-key
4. Which serial data communication mode is multiprocessor 9-bit UART []
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a. MOV b. MVN c. LDR d. TEQ
7. "Overflow" condition code in ARM is []
a. VC b. VS c. EQ d. EZ
8. Control sequence of D/A conversion is []
a. CS then WR b. WR then CS c. CS then WR then RD d. WR then RD
9. A/D conversion time is []
a. 1 μ s b. 2 μ s c. 3 μ s d. 4 μ s
10. A/D converters use ____ types. []
a. SAR b. flash c. magnetic d. bubble

Cont.....2

II Fill in the blanks

11. The barber problem is an example of _____
12. To access a shared resource, there should be a mechanism so that there is discipline. This is known as _____ synchronization
13. Counting semaphore will have an integer value greater than _____
14. A _____ mechanism is used to get your software into your target for debugging purposes.
15. A _____ understands the same C language as a native compiler and as assembly language that is specific to your target microprocessor.
16. BDM stands for _____
17. The _____ state means that this task has not got anything to do right now, even if the microprocessor becomes available.
18. The _____ program can be modified to use a timer to generate associated with the key-down de bounce time and the "all-up" delay.
19. _____ displays include numeric and alphanumeric arrays.
20. A SHARC instruction consists of _____ bits

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Set No. 3

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[IT]

Objective Exam

Name: _____ Hall Ticket No.

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Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

I Choose the correct alternative:

1. A ____ keyboard is interfaced with 8051 microcontroller. []
a. 16-key b. 32-key c. 64-key d. 128-key
2. Which serial data communication mode is multiprocessor 9-bit UART []
a. mode 0 b. mode 1 c. mode 2 d. mode 4
3. For both counting semaphore and binary semaphore a _____ is used to hold processing waiting on the semaphores []
a. stack b. queue c. dequeue d. circular queue
4. ARM comparison instruction is []
a. MOV b. MVN c. LDR d. TEQ
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a. preemptive b. non preemptive c. interpret d. exception
10. The program ____ displays characters found in locations ch1 to ch4 on four common-cathode seven- segment displays. []
a. svnseg b. lcdisp c. lcdisp2 d. svnseg3

Cont.....2

II Fill in the blanks

11. Counting semaphore will have an integer value greater than _____
12. A _____ mechanism is used to get your software into your target for debugging purposes.
13. A _____ understands the same C language as a native compiler and as assembly language that is specific to your target microprocessor.
14. BDM stands for _____
15. The _____ state means that this task has not got anything to do right now, even if the microprocessor becomes available.
16. The _____ program can be modified to use a timer to generate associated with the key-down de bounce time and the "all-up" delay.
17. _____ displays include numeric and alphanumeric arrays.
18. A SHARC instruction consists of _____ bits
19. The barber problem is an example of _____
20. To access a shared resource, there should be a mechanism so that there is discipline. This is known as _____ synchronization

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I Choose the correct alternative:

1. For both counting semaphore and binary semaphore a _____ is used to hold processing waiting on the semaphores []
a. stack b. queue c. dequeue d. circular queue
2. ARM comparison instruction is []
a. MOV b. MVN c. LDR d. TEQ
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a. mode 0 b. mode 1 c. mode 2 d. mode 4

Cont.....2

II Fill in the blanks

11. A _____ understands the same C language as a native compiler and as assembly language that is specific to your target microprocessor.
12. BDM stands for _____
13. The _____ state means that this task has not got anything to do right now, even if the microprocessor becomes available.
14. The _____ program can be modified to use a timer to generate associated with the key-down de bounce time and the "all-up" delay.
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