

**VLSI DESIGN
KEYS**

I Choose the correct alternative:

1. B
2. A
3. A
4. B
5. A
6. B
7. B
8. A
9. C
10. B

II Fill in the blanks:

11. Carry-look ahead adder
12. Logic synthesis
13. System partitioning
14. Joint Test Acton Group
15. Constraints
16. Observability
17. Switchbox routing
18. Test vector.
19. Scan path
20. Global routing